

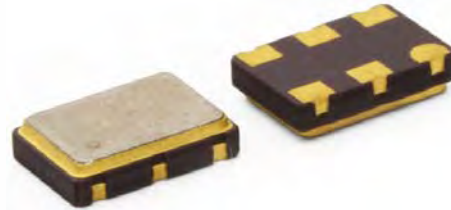


Model 656C

Advanced PLL HCMOS Clock

Features

- Ceramic Surface Mount Package
- Low Phase Jitter Performance, 600fs Typical
- Advanced PLL Design w/ Low Fundamental Crystal
- Frequency Range 10 – 250MHz *
- +2.5V or +3.3V Operation
- Output Enable Standard
- Tape and Reel Packaging, EIA-418



Part Dimensions:
7.0 × 5.0 × 2.0mm • 178.462mg

Applications

- Broadcast Video Systems
- Storage Area Networking
- Broadband Access
- PCI Express
- Networking Equipment
- Ethernet/GbE/SyncE
- Fiber Channel
- Test and Measurement

Standard Frequencies
 - 77.76MHz - 106.25MHz
 - 100.00MHz - 125.00MHz

* Check with factory for availability.

Description

CTS Model 656C is a low cost, high performance PLL clock oscillator supporting HCMOS output. Employing the latest IC technology, M656C has excellent stability and low phase jitter performance.

Ordering Information

Model	Output Type	Frequency Code [MHz]	Frequency Stability	Temperature Range	Supply Voltage	Packaging
656	C	XXX or XXXX	3	I	3	T
		Code Frequency Product Frequency Code ¹		Code Temp. Range C -20°C to +70°C I -40°C to +85°C		Code Packing T 1k pcs./reel
	Code Output C HCMOS		Code Stability 6 ±20ppm ² 5 ±25ppm 3 ±50ppm		Code Voltage 2 +2.5Vdc 3 +3.3Vdc	

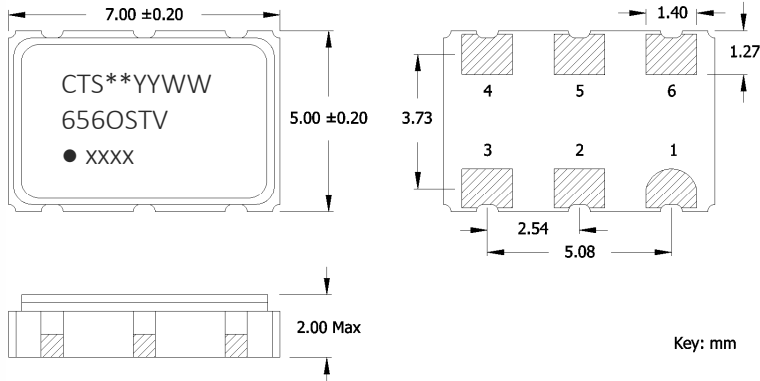
Notes:

- 1] Refer to document 016-1454-0, Frequency Code Tables.
3-digits for frequencies <100MHz, 4-digits for frequencies 100MHz or greater.
- 2] Consult factory for availability of 6I Stability/Temperature combination.

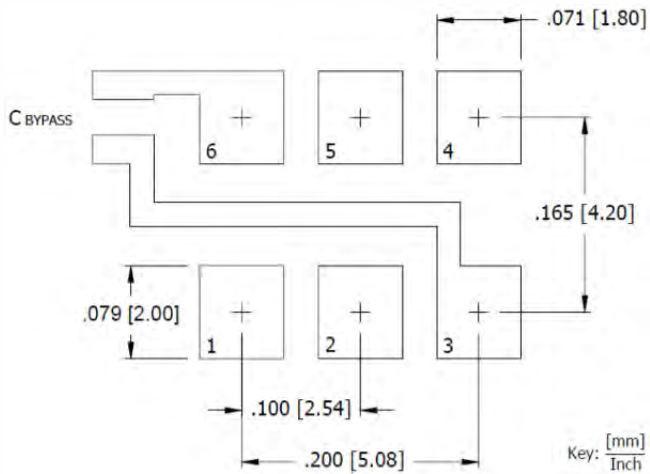
**Not all performance combinations and frequencies may be available.
Contact your local CTS Representative or CTS Customer Service for availability.**

SHENZHEN YIJIN ELECTRONICS CO: LTD TEL: 0755-27876565

18924600166 QQ: 857950243 <http://www.vc-tcxo.com>



1. ** - Manufacturing Site Code.
 2. YYWW – Date Code; YY – year, WW – week.
 3. O – Output Type; C = HCMOS.
 4. ST – Frequency Stability/Temperature Code.
[Refer to Ordering Information]
 5. V – Voltage Code; 3 = 3.3V, 2 = 2.5V.
 6. xxxx – Frequency Code.
3-digits, frequencies below 100MHz
4-digits, frequencies 100MHz or greater
- [See document 016-1454-0, Frequency Code Tables.]



1. JEDEC termination code (e4). Barrier-plating is nickel [Ni] with gold [Au] flash plate.
2. Reflow conditions per JEDEC J-STD-020; +260°C maximum, 20 seconds.
3. MSL = 1.

1	EOH	Enable
2	N.C.	No Connect
3	GND	Circuit & Package Ground
4	Output	RF Output
5	N.C.	No Connect
6	V _{CC}	Supply Voltage